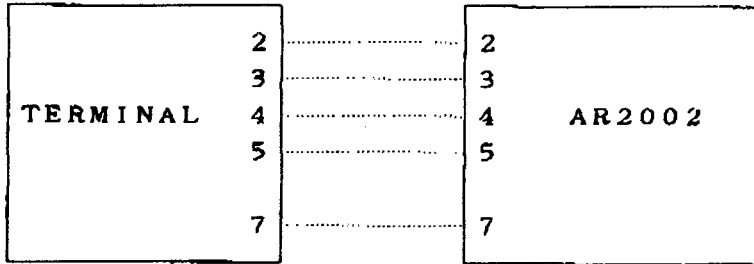


CIRCUIT DIAGRAM FOR "RC PACK"

INTERFACE UNIT FOR AR2002

1) RS232C COMMUNICATION (ASCII CODE)



2) RS232C SET UP

SPEED . . . . . 1200/2400/4800/9600 BPS  
 DATA . . . . . 8 BIT  
 STOP . . . . . 2 BIT  
 PARITY . . . . . NON  
 XON . . . . . NON

3) DATA FORMAT (TERMINAL TO AR2002)

3-1 DATA WRITE

RX FREQ . . . \_\_\_\_\_ (25.0000-1300.0000)  
 MODE . . . . . N (NFM)  
                   W (WFM)  
                   A (AM)

EXAMPLE

80W[CR] . . . . . 80MHz WFM  
 82.5[CR] . . . . . 82.5MHz (no change of MODE)  
 A[CR] . . . . . MODE change to AM

3-2 DATA READ OUT

AGC LEVEL . . . . . L [CR]  
  
           A [CR] . . . . . LOW LEVEL  
           B [CR] . . . . .  
           C [CR] . . . . .  
           .  
           .  
           P [CR] . . . . . HIGH LEVEL

SQUELCH ON/OFF . . . . . S [CR]

1 [CR] . . . . . SQUELCH ON  
 0 [CR] . . . . . SQUELCH OFF

